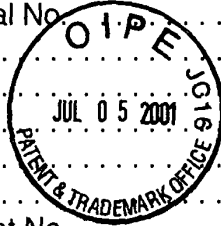




19/10/01  
7/24/01  
A. Walker

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/292,132  
Filing Date April 14, 1999  
Inventor Salman Akram et al.  
Assignee Micron Technology, Inc.  
Group Art Unit 2812  
Examiner S. Mulpuri  
Attorney's Docket No. MI22-1171  
Title: Methods of Forming a Transistor Gate



**INFORMATION DISCLOSURE STATEMENT**

To: Assistant Commissioner for Patents  
Washington, D.C. 20231

From: D. Brent Kenady (Tel. 509-624-4276; Fax 509-838-3424)  
Wells, St. John, Roberts, Gregory & Matkin P.S.  
601 W. First Avenue, Suite 1300  
Spokane, WA 99201-3828

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Dear Sir:

The Examiner's attention is directed to the reference which is listed on the attached Form PTO-1449 and a copy of which is attached.

Citation of this reference is respectfully requested.

A check in the amount of \$180 is enclosed to cover the fee specified under 37 C.F.R. § 1.17(p).

07/13/2001 WBERHE 00000011 09292132

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22-00-00

180.00 OP  
22-00-00

Respectfully submitted,

Dated: 7-5-01

By: D. Brent Kenady  
D. Brent Kenady  
Reg. No. 40,045

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